

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kevin M. Conley et al.
Title: Flash Controller Cache Architecture
Application No.: 10/796,575 Filing Date: March 8, 2004
Examiner: Tuan T. NGUYEN Group Art Unit: 2824
Docket No.: SNDK.344US0 Conf. No.: 1109

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant(s) call(s) the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

According to 37 C.F.R. 1.98(2)(ii), copies of the U.S. Patents and U.S. Published Patent Applications documents are not required and are therefore not enclosed. Copies of the listed foreign patent documents or Other Art are enclosed.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required.

Respectfully submitted,


James S. Hsue
Reg. No. 29,545

October 24, 2006
Date

U.S. Department of Commerce, Patent and Trademark		Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		SNDK.344US0	10/796,575
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(Use several sheets if necessary)		Conley et al.	1109
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		March 8, 2004	2824

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	5,043,940	8/27/91	Harari			
	2	5,070,032	12/3/91	Yuan et al.			
	3	5,095,344	3/10/92	Harari			
	4	5,313,421	5/17/94	Guterman et al.			
	5	5,343,063	8/30/94	Yuan et al.			
	6	5,418,752	5/23/95	Harari et al.			
	7	5,570,315	10/29/96	Tanaka et al.			
	8	5,661,053	8/26/97	Yuan			
	9	5,677,872	7/14/97	Samachisa et al.			
	10	5,774,397	6/30/98	Endoh et al.			
	11	5,712,179	1/27/98	Yuan			
	12	5,909,449	6/1/99	So			
	13	5,930,167	7/27/99	Lee et al.			
	14	6,026,027	2/15/00	Terrell, II et al.			
	15	6,046,935	4/4/00	Takeuchi et al.			
	16	6,735,661	5/11/04	Gelke et al.			
	17	6,941,412 B2	09/2005	Gongwer et al.			
	18	6,721,843 B1	04-2004	Estakhri			
	19	6,968,421 B2	11-2005	Conley			
	20	6,249,841 B1	6/19/01	Sikes et al.			
	21	6,456,528	9/24/02	Chen			
	22	6,522,580	2/18/03	Chen et al.			
	23	6,717,847 B2	4/6/04	Chen			
	24	6,763,424 B2	7/13/04	Conley			
	25	6,925,007 B2	8/2/05	Harari et al.			

U.S. Published Patent Application Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	26	2005/0257120 A1	11/2005	Gorobets et al.			
	27	2005/0268025 A1	12/2005	Smith et al.			

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Foreign Patent Documents								
								Translation
		Document	Date	Country	Class	Subclass	Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	28	Imamiya, K. et al., "A 125-mm ² 1 Gb NAND Flash Memory With 10-Mbytes/s Program Speed," 2002 IEEE						
Examiner		Date Considered						
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								